

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|------------------|------------------|---------|------------------|
| L1 | 4191 | @ad<="19990317" switch memor\$2 interfac\$2 data near port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:42 |
| L2 | 104 | @ad<="19990317" switch same memor\$2 interfac\$2 data near port\$2 management near unit | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:42 |
| L3 | 61 | @ad<="19990317" switch same memor\$2 interfac\$2 data near port\$2 management near unit internal near memory | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:43 |
| L4 | 22 | @ad<="19990317" switch same memor\$2 interfac\$2 data near port\$2 management near unit internal near memory external near memory | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:45 |
| L5 | 18 | @ad<="19990317" switch same memor\$2 interfac\$2 data near port\$2 management near unit internal near memory external near memory data near rat\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:45 |
| L6 | 16 | @ad<="19990317" switch same memor\$2 interfac\$2 data near port\$2 management near unit internal near memory external near memory (data near rat\$2) same port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:50 |

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| L7 | 16 | @ad<="19990317" switch same memor\$2 management near unit internal near memory external near memory (data near rat\$2) same port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:50 |
| L8 | 0 | @ad<="19990317" switch same memor\$2 management near unit internal near memory external near memory (data near rat\$2) same port\$2 communication\$2 near channel | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:51 |
| L9 | 0 | @ad<="19990317" switch same memor\$2 management near unit internal near memory external near memory (data near rat\$2) same port\$2 communication\$2 near (channel or link) | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:51 |
| L10 | 16 | @ad<="19990317" switch same memor\$2 management near unit internal near memory external near memory (data near rat\$2) same port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:52 |
| L11 | 16 | @ad<="19990317" switch same memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:52 |

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| L12 | 16 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:04 |
| L13 | 0 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 370/230,231,232, 400.ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:56 |
| L14 | 0 | "395""."7,395""."71,477, 465".ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:56 |
| L15 | 0 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 370/395.7,395.71, 477,465.ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:56 |
| L16 | 2 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 345/501,502,520, 539,541,543,544.ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 09:59 |
| L17 | 1 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$2 710/65,68,7.ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:01 |

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| L18 | 16 | @ad<="19990317" switch with memor\$2 management near unit internal near memory external near memory (data near rat\$2) with port\$3 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:05 |
| L19 | 22 | 4 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:04 |
| L20 | 16 | @ad<="19990317" switch with memor\$3 management near unit internal near memory external near memory (data near rat\$2) with port\$3 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:05 |
| L22 | 17 | (switch with memor\$3 management\$2 near unit internal near memory external near memory (data near rat\$2) with ports).clm. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:06 |
| L23 | 0 | @ad<="19990317" (switch with memor\$3 management\$2 near unit internal near memory external near memory (data near rat\$2) with port\$3).clm. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:08 |
| L24 | 0 | @ad<="19990317" switch with memor\$3 management\$2 near unit internal near memory external near memory (data near rat\$2) with port\$3 370/463,462,360, 362.ccls. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:08 |
| L25 | 60 | kadambi-shiri.in. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:10 |
| L26 | 113 | ambe-shekhar.in. | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:10 |

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| L27 | 16 | @ad<="19990317" switch with memor\$3 management\$2 near unit internal near memory external near memory (data near rat\$2) with port\$2 | US-PGPU B; USPAT | AND | OFF | 2007/07/17 10:53 |
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